

# ECE 221 Digital Logic Design Homework Assignment #8

**Due Date: November 12, 2003**

Background Reading: Chapter 8

Assignment: 1  
4  
7  
9

Extra Problem: Given the schematic on the next page, determine what two 3-bit sequences it will recognize (show all of your work).

Name: \_\_\_\_\_

Honor Code Pledge:

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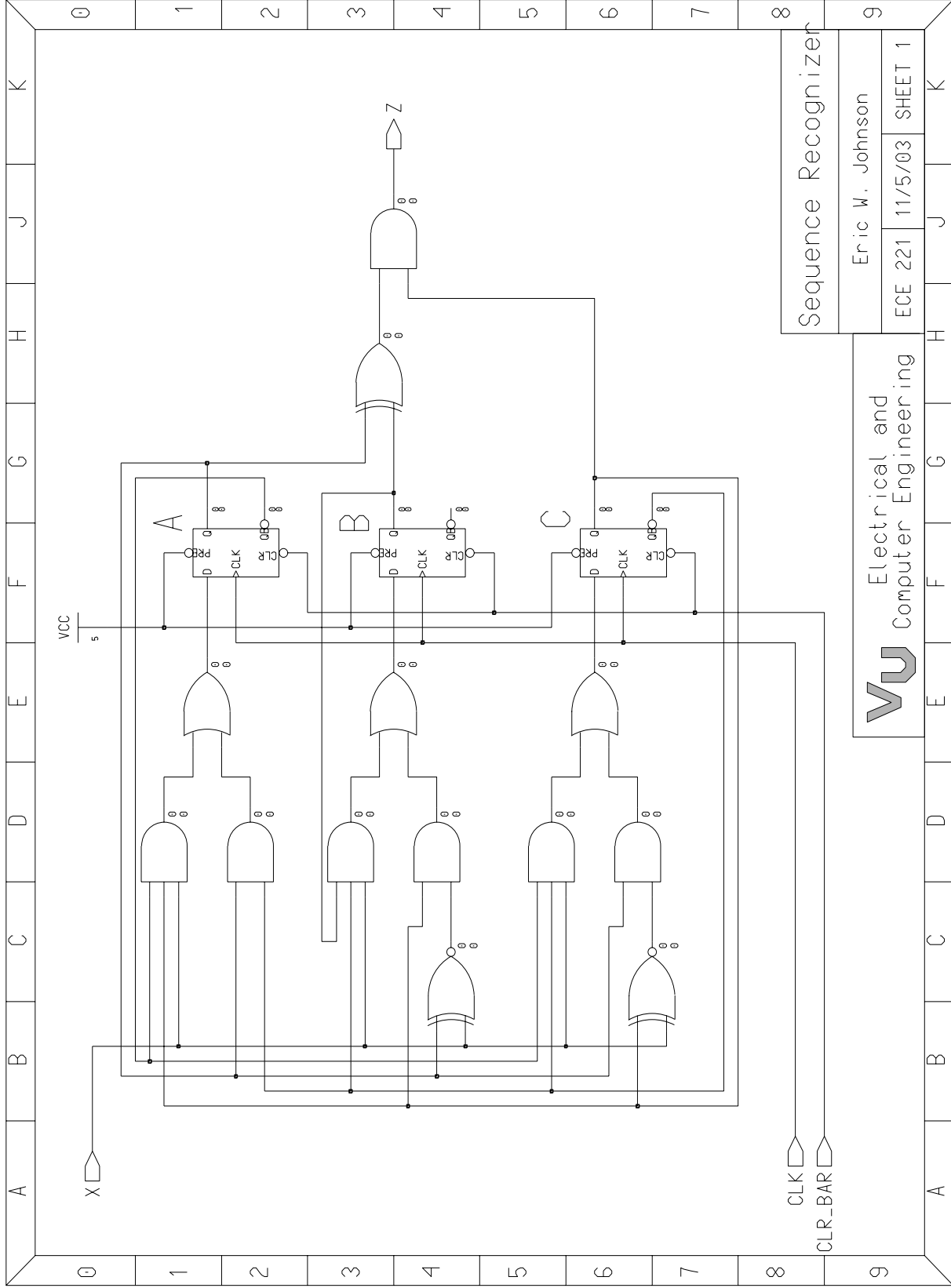
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Signature: \_\_\_\_\_

Please staple this sheet to the front of your assignment.

Problem 1	/20
Problem 4	/10
Problem 7	/15
Problem 9	/10
Extra Problem	/45
Total	/100



Sequence Recognizer

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ECE 221 11/5/03 SHEET 1



A B C D E F G H J K

0 1 2 3 4 5 6 7 8 9

CLK CLR\_BAR

A B C D E F G H J K

0 1 2 3 4 5 6 7 8 9