

ECE 221 Digital Logic Design Homework Assignment #6

Due Date: October 13, 2003

Background Reading: Chapter 5, Sections 5.2-5.4

- Assignment:
- 4.13 c - Use Espresso to find both SOP and POS functions. Which is better? Turn in input and output files.
 - 4.17 c - Use Espresso to find SOP minimized function and answer the question posed by part c. Turn in input and output files. To run individual function minimizations you need to use the -Dso option.
 - 5.2
 - 5.3
 - 5.5 a (timing diagrams = waveforms)

Name: _____

Honor Code Pledge:

Signature: _____

Please staple this sheet to the front of your assignment

Problem 4.13 c	/25
Problem 4.17 c	/25
Problem 5.2	/15
Problem 5.3	/15
Problem 5.5 a	/10
Total	/90