

ECE 221 Digital Logic Design

Homework Assignment #4

Due Date: September 17, 2003

Background Reading: Chapter 3, Sections 3.5-3.9, Chapter 4, Section 4.1

Assignment: 3.9 c,d
3.11
3.13
3.14 c,d,e,f
3.15
3.18
4.1 a

Name: _____

Honor Code Pledge:

Signature: _____

Please staple this sheet to the front of your assignment.

Problem 3.9 c,d	/10
Problem 3.11	/15
Problem 3.13	/10
Problem 3.14 c,d,e,f	/20
Problem 3.15	/10
Problem 3.18	/10
Problem 4.1 a	/20
Total	/95